

University of Southern California
VITERBI SCHOOL OF ENGINEERING

Master of Science in Electrical Engineering (Computer Architecture)

Program Learning Objectives

The purpose of the USC Viterbi School of Engineering Master of Science in Electrical Engineering (Computer Architecture) program, referred to as MSEE-CA, is to prepare students for high-level professional employment in any sector of the electrical engineering arena that incorporates computer architectural techniques; or, to pursue advanced graduate studies focusing on related problems in the field. Graduates might pursue computer-architecture-related employment or advanced graduate study relating to computer architecture, computer engineering, computer networks and systems, VLSI/CAD, or software engineering.

- Upon completion of the USC Master of Science in Electrical Engineering (Computer Architecture) program, students will be able to demonstrate broad understanding of computer architecture, including computer systems and software, computer networks, and VLSI/CAD.
- Upon completion of the USC Master of Science in Electrical Engineering (Computer Architecture) program, students will be able to apply critical principles and skills pertinent to MSEE-CA duties in their employment and professional practice.
- Upon completion of the USC Master of Science in Electrical Engineering (Computer Architecture) program, students will be able to work in diverse global contexts and apply universally respectful and globally centric practices pertinent to MSEE-CA duties in international and domestic contexts.
- USC students enrolled in the Master of Science in Electrical Engineering (Computer Architecture) program will demonstrate understanding of contemporary research questions, results, and areas of application relating to diverse aspects of computer architecture.